



JAIPUR ENGINEERING COLLEGE
AND RESEARCH CENTRE

INTERNAL ACADEMIC REPORT AUDIT CORRECTION REPORT

Academic year 2019-20

DQAC		DATE	10/07/2019
PROCESS	Academic Process (Electrical Department)		
Auditors	1) Dr. Prerak Bhardwaj 2) Mr. Atul Kulshrestha 3) Mr Gopal Tiwari	Auditees	EE Faculties
Observers			

Sr. No	Observation	Type	Correction
1	Course file (Faculty)		Status Check By Gopal Tiwari Dy HOD
2	Academic Diary (Faculty)		Status Check By Gopal Tiwari Dy HOD
3	Defaulter list (All CC's)		Signed by HOD
4	Mapping of all subjects		Status Check By Gopal Tiwari Dy HOD
5	Industry feedback data		Maintained by TPO
6	Alumni Feedback		Maintained by TPO

Sr. No	Observation	Type	Remark
1	All Academic process	Departmental Policy	Maintained by Departmental Committee – MOM chaired by HOD
2	Course File		Maintained by individual faculty
3	PO and PEOs and CO and PSO's		Common
4	Mapping		Faculty
5	Student feedback analysis index		Analysis – action taken by HOD
6	Industry feedback analysis index		Maintained by TPO
7	Alumni feedback analysis index		Maintained by TPO
8	Remedial Lectures		Maintained by TT I/C instructed by HOD
9	Advance Learners		Highlighted in diary with proof by the individual faculty
10	Slow learners' efforts taken		Register is maintained by Respective faculty with action taken with proof by individual faculty
11	Add on Courses		Maintained by SDO and TPO
12	Seminars/Guest Lectures		Maintained by SDO

13	Social Activities/ Ethical/Moral value education		Maintained by SDO
14	Higher Education data		Maintained by TPO
15	Internship data		Maintain by SDO / TPO / and respective faculty
16	Student final year project		Project coordinator
18	All files (Sample tested)	OK	OK
19	Previous Students punched old files, exam record		Maintained by EXAM I/c
20	Existing submission		Going on
21	Training needed identification teaching, nonteaching		Maintained in soft copy by the HOD / Departmental Committee
22	Budget details	In Progress	HOD – DY HOD and all lab in charges with their lab requirement
23	Departmental Library details	Found OK	Maintained in soft copy & hard copy
24	FDP/Publications	OK	Maintained by Shailendra Shrivastva
25	Curricular and co-curricular activities	OK	Maintained by SDO
26	Teaching/ Non-teaching appraisal review	OK	HOD –DY HOD

Name

Signature

Dr. Prerak Bhardwaj

Mr. Atul Kulshrestha

Mr. Gopal Tiwari

Prerak
Atul
Gopal

Prerak
HOD EE
Head of the Department
Electrical Engineering
JECRC Jaipur

Am
Shailendra
PRINCIPAL
Jaipur Engineering College &
Research Centre
Tonk Road, Jaipur-302022



JAIPUR ENGINEERING COLLEGE
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JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

Format for Academic Audit (2019-20)

Name of the Department: Electrical Engineering

Date: 16 September 2019

Name, Designation and Address of Academic Audit Experts:

Members of Staff Present:

1. Dr. Prerak Bhardwaj
2. Mr. Atul Kulshrestha
3. Mr. Vishal Sharma
4. Mr. L Senthil

a) Reviewed by Dr. Amit Shrivastva, Professor Electrical Engineering Poornima University, Jaipur

b) Reviewed by Industry expert Mr. BS Jha, Executive Engineer (Retd.) RVPNL Jaipur.

Criterion	Items	Verification Yes / No	Comments	Suggestions for improvement
I. Curriculum	Steps followed in the designing of syllabus & curriculum	YES	Affiliated to Rajasthan Technical University(RTU), Kota	Benchmarking should be done with leading institutes/universities and mentioned.
	Contents of the Curriculum	YES	Affiliated to RTU. The Dept is following OBE practices and after internal assessment, the department identifies the weak students and the fast learners. For weak students, the department undertakes mentoring and remedial classes. The Fast learners are encouraged to participate in conferences/seminars etc and the participation fees is either waved off or paid by the college itself. The Fast learners are motivated to be a	The approach of identifying slow learner in every semester is done in the department and extra efforts are done on them and advance learners are motivated and efforts are taken to increase the learning capability.



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			part of the organising committee and financial assistance is given on Pan India basis and are encouraged to participate on co-curricular activities outside college. The college provides the cost of the raw materials as well as transport facility.	
	Validation done	YES	Affiliated to RTU	-
	IDC / EDC	YES	Motivating students for start-up and applying for patents	Students from 2 nd year must be motivated and encouraged for start-ups
	Credits allotted / distribution – logic	YES	Mapping is done	Mention how the mapping was done, which tool used, who were involved, when was it done.
2. Curriculum Transaction	Teaching methods & teaching aids	YES	<ol style="list-style-type: none"> 1. Lectures 2. Laboratories 3. Guest lectures 4. Technical Activities 5. Brain Zest 6. Social Activities 	<p>Guest Lectures should be organised in the area of industrial automation and latest technological developments in the field of renewable energy.</p> <p>Also Brain Zest and Social Activities like blood donation camp, and SDP donors must be frequently organised.</p>
	E-learning modules	YES	<ol style="list-style-type: none"> 1. E-BOOKS 2. ICT Courses 3. NPTEL/Swayam 4. Faculty e-Content 	<p>Advisable to elaborate. Categories of e-books, which ICT courses, what kind of e-content. Provide few examples if all not possible to be listed.</p>
	Project work UG/PG	YES	<p>Paper Presentation Poster Presentation Skill development activity Industry institute interaction Projects done in the area of automation, artificial intelligence, machine learning, hybrid electrical</p>	<p>Projects started in third year will give early outputs in terms of research paper publication and patents and start-ups.</p>



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			vehicle, social, experimental and analysis.	
	Internal assessment – components – Uniqueness	YES	Mid Term Evaluated on the basis of Course Outcome (CO) and based on CO, slow learners and advance learners were identified.	Mention the components of assessment like tests, presentations, etc.
	Student support – remedial coaching	YES	Identification of weak students by means of - Oral questionnaire and Query session	Mentoring to be done on one to one students.
	Parents Open House Meeting –evaluation of student's progress	YES	Performance report is discussed with students & their parents on regular basis.	The frequency of meeting with parents should be increased.
	Feedback from students	YES	Communicated through program coordinator to faculties	Feedback mechanism was appraised and also analysis and action taken reports were thoroughly judged by senior faculty members of the department. Efforts should be made for improvement in department based on analysis.
	Steps taken on the feedback	YES	Feedback analysis is done.	Use statistical tool for the analysis.
3.Faculty Profile	Projects completed / on going	YES	Ongoing	Final year projects are ongoing in different areas of Electrical engineering. Motivate Students to apply for funded projects.
	Seminars / conferences attended	YES	Many faculties attended conferences and seminars	Faculty members are motivated and attended several seminars and conferences of repute.
	Papers / articles / published	YES	Many faculties have excellent publication	Faculties are advised for quality publications/ and edited books
	FDP / RC / OC / Training	YES	Faculties are encouraged to attend the same and organise	Faculties are advised to participate in more than 5 days FDP programs.



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	Program Workshop /		FDP at least once in semester for overall development.	
	Preparation of E-learning materials / Content	YES	Students as well as faculties enrol the courses	This item is about how the e-learning material is prepared. Write about who prepares, who finalizes and approves.
4. Profile of Students	Demand ratio (Applications received Vs Sanctioned Strength)	YES		
	Students involvement in extra-curricular & Cocurricular activities	YES	Majority of students are involved in extra-curricular & Cocurricular Activities.	Technical activities, national and international conferences are appreciated but more tie-ups for IEEE based conferences are required.
	Study tour / industrial visits / exhibitions / Internship / Training	YES	The institute has a facility "Bhraman" under this facility students are taken to various places and in different industries Department organises students visit nearby area in Jaipur on regular basis. Students showcasing their project work in the institute are given certificate and prizes. Students do mandatory internship training as prescribed by RTU.	More industrial trips nearby and inside Jaipur must be planned and implemented.
5. Infrastructure in the Department	No. of class rooms	YES	6	Sufficient classroom as per curriculum prescribed by RTU
	No. of laboratories	YES	7	As per curriculum by RTU
	No. of computers – for teachers	YES	3	Less computers allotted, more laptops maybe allotted to faculty on compulsory Buy Back Scheme.



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	No. of computers – for students	YES	CAD LAB	Students are allowed to access the computers in laboratory.
6. Activities of the Department	MoU's signed	YES	The JECRC has made MOU with the following companies in past few years. 1. Techinest, Jaipur 2. Engineers Academy Jaipur	Much appreciated regarding active MOU'S which leads to increase in industry institute interaction.
	Consultancy	No	Suggestion for work towards in this field.	Dept. should approach industries for consultancy work.
	Collaborations	YES	Department has collaboration with Techinest	Students chapters like IEEE, ETC should be started in the department
	Association Meetings	NO	-	-
	Guest lectures	YES	In last three years department of Electrical Engineering invited lot of eminent Academicians and Industrialists.	Guest lectures are organised wherein eminent personalities from the corporate world are invited to share real life problems with students.
	Conference / Seminar / Workshop conducted	YES	In last three years department of Electrical Engineering organised a few National and International conferences. The selected papers were published in reputed Journal.	Conferences/seminars and workshop leads to latest awareness in students. Which leads to improve research facilities, inculcate research culture among faculties and students.
	Extension Activity	Yes	The dept. actively participated in various programs like Swacch Bharat Abhiyan, tree plantation drive, blood donation camps, SDP donors, sports, dance, singing, street play etc. Celebration of Engineer's day, Teachers day, Independence day, republic day and women's day.	Students must involve in extension activities for overall growth and development.



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	Interaction with Industry / Research Centres / Educational Institutions	YES	For internship students are applying for paid internship through Intershala and some students get training through that, as per affiliating university. However, 15 days training is mandatory for 1 st and 2 nd year students and one month for 3 rd year students. Electrical department visited various industry along with students	Encourage students to undertake industry sponsored projects and participate in projects competition at national and international level.
	Newsletters / Magazine	YES	Department of Electrical Engineering highlighted the various activities of the department by faculty members and students in the form of Magazine on quarterly basis.	Motivate and encourage the students to publish their own student magazine.
	Placement	YES	College provides the skill based training and technical training to students.	Training & placement activities reinforce the student's skill and help them to acquire industry specific knowledge from trained faculty and experts from outside. Training positively influences the performances, productivity and self-esteem of the students

Please comment on SWOT Analysis:

SWOT Analysis:

Strength:

1. Faculty members are young and dedicated for handling the classes, laboratory and extra curriculum activities.
2. Have good student Faculty ratio-as per AICTE guideline
3. Strong social commitments by students and faculties
4. Project based learning and collaborative learning for students



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Weakness:

1. Centre of excellence does not exist.
2. Departmental Career Development cell
3. The current syllabus is partially based on the current needs of industry.
4. Sponsored research projects from any funding agency are minimal
5. Consultancy and testing from the industry are minimal
6. Placements in core companies are average.

Opportunities:

1. Department follows Outcome based education
2. Provide jobs for fast learners as well as slow learners.
3. Motivate the students for publication
4. Motivate and Provide platform for start-up & patent
5. Motivate and provide platform for social activities.

Challenges:


1. Average employment opportunities for fresh graduates
2. Gap between curriculum and industry requirements
3. Bringing slow learner to the level of fast learners
4. International collaboration with reputed institutes/ Industries

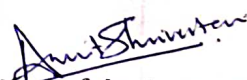
Best Practice (s) / Innovations of the Department: (need to mention more. Just one best practice means the college doesn't have much to position itself as a good place for study. Add information related to courses, faculty strength etc)


1. Team Brain Zest participate at national level.
2. Identifying thrust areas and guidance by senior faculty to juniors for confident and satisfactory delivery in the classroom for that content.
1. To improve the satisfaction index of students for teaching and learning activity.
2. Mentoring of weak students.

Future of the Department:

1. Setting of Industry oriented lab in the department.
2. Industry specific courses to be introduced soon.
3. Hands on practice on frequent basis on automation and green energy.


Signature of the HoD with Seal
Head of the Department
Electrical Engineering
JECRC Jaipur


Signature of the Academic Audit Experts




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Jaipur
2022

JAIPUR ENGINEERING COLLEGE AND RESEARCH CENTRE

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**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Electrical Machine lab - I

Lab In charge: Mr Sunil Kumar Sharma

Lab Technician: Mr Sombir Sharma

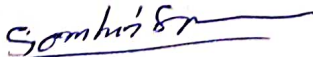
Audit Date: 04/09/2019

Session: 2019-2020

Members of Staff Present:

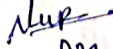
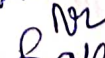
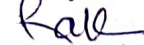
1. Ms Nupur Yadav
2. Mr Atul Kulshrestha
3. Mr Rakesh Kardam


Sr. No.	Comments	Action Taken	Remark
1	Stock register verified and all item are matched with stock register entry	Letter given for Physical verification to lab in charge	Physical verification has been done
2	Display board maintained but lab allotment process is missing	Letter send to lab technician to maintain the notice board and display the lab allotment process.	Display board updated
3	Wiring of the machines and connections are found loose and missing	Letter send to lab in charge and lab technician for maintaining the lab and asked to prepare the note sheet for consumables if required	Compliance of maintenance is obtained and requirement sheet for consumables is generated
5	Bill available	No action require	Advice to keep maintain the bill file


Signature of Lab Technician


Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) 
- (2) 
- (3) 


Signature of the HOD

Head of the Department
Electrical Engineering
JECRC Jaipur



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Department of Electrical Engineering Lab Audit for year (2019 - 20)

Name of the Department: Electrical Engineering
Name of Laboratory: Electrical Circuit Design Lab
Lab In charge: Mr Vishnu Dutt Sharma
Lab Technician: Mr Dileep Singh Jadoun
Audit Date: 03/09/2019

Session: 2019-2020

Members of Staff Present:

1. Mr Rahul kumar Malec
2. Ms Nupur Yadav
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Kits are required as per RTU syllabus	Letter send to lab in charge and lab technician to prepare Note sheet according to the RTU syllabus and generate requirement	Requirement generated
2	Lab manuals are not updated	Letter send to lab in charge regarding update of the lab manual and also increase the availability of lab manual copy for students	Lab manual update
3	Display Board maintained but lab allotment process is missing	Letter send to lab technician to maintain the notice board and display the lab allotment process.	Display board updated
4	Consumables like solder iron, bread board are missing	Latter send to lab in charge and lab technician for proper maintenance of the lab and preparing note sheet according to Lab requirement	Requirement has been full fill

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Signature of Lab Technician

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Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) *Rahul*
- (2) *Nupur*
- (3) *Rakesh*

Jadoun

Signature of the HOD
Head of the Department
Electrical Engineering
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**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Electrical Machine - II

Lab In charge: Mr Atul Kulshrestha

Lab Technician: Mr Sombir Sharma

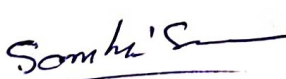
Audit Date: 03/09/2019

Session: 2019-2020

Members of Staff Present:

1. Mr L. Senthil
2. Mr Gopal Tiwari
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Stock register verified and maintained properly.	Letter for Physical verification given to lab in charge.	Physical verification has been done
2	Display board maintained but lab allotment process missing.	Letter send to lab technician to maintain the notice board and display the lab allotment process.	Display board updated
3	Lab manuals are not maintained properly.	Letter send to lab in charge regarding updating of the lab manuals	Lab manual update as per syllabus
4	Lab covers all syllabus experiments properly	No action require	NA
5	Bill available	No action require	Advice to keep maintain the bill file


Signature of Lab Technician

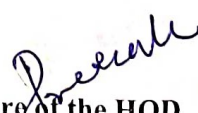
Signature of Lab In charge

Signature of the Lab Audit Experts

(1)

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(3)


Signature of the HOD

Head of the Department
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**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Power Electronics Lab

Lab In charge: Ms Neha Agrawal

Lab Technician: Mr Suresh Kumar Gurtjar

Audit Date: 03/09/2019

Session: 2019-2020

Members of Staff Present:

1. Mr Sunil Kumar Sharma
2. Mr Vishal Sharma
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Students record available & checked properly	Letter send to faculty to keep 5 best Record of last year.	5 best record of last year are available.
2	Kits are not working (IGBT and MOSFET characteristics)	Lab in charge is instructed to submit the maintenance letter for these kits.	Maintenance is done and kit is working
3	Consumable item need maintenance like solder iron, patch cords connection with the wires	Letter send to lab in charge and lab technician for prepare NOTE SHEET for consumables and ask to check the connection of each kits properly.	Requirement has been full filled.
4	Stock register verified in all terms and condition is good.	Letter for Physical verification given to lab in charge	Physical verification has been done
5	Bill available	No action require	Advice to keep maintain the bill file


Signature of Lab Technician



Signature of Lab In charge


Signature of the Lab Audit Experts

(1)

(2)

(3)


Signature of the HOD

Head of the Department
Electrical Engineering
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**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Measurement lab

Lab In charge: Mr Ashok Singh

Lab Technician: Mr Vishwas Verma

Audit Date: 12/09/2019


Session: 2019-2020

Members of Staff Present:

1. Ms Sonali Chadha
2. Mr Rahul kr. Malee
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Stock register verified and maintained properly.	Letter send for Physical verification to lab in charge	Physical verification has been done
2	Students record available & checked properly	Letter send to faculty to keep 5 Record of last year	5 best record of last year are available.
3	Crompton potentiometer and earth tester is not working	Maintenance letter is issued to lab technician regarding proper maintenance of equipment	Maintenance is done and kits are working
4	Display Board maintained but safety instructions for the lab is missing	Letter send to lab technician to maintain the notice board display	Display board updated
5	Covers all syllabus	No action require	NA


Signature of Lab Technician

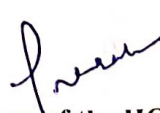

Signature of Lab In charge

Signature of the Lab Audit Experts

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Signature of the HOD

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**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Power System – I Lab

Lab In charge: Mr Shailendra Srivastava

Lab Technician: Mr Vishwas Verma

Audit Date: 14/09/2019

Session: 2019-2020

Members of Staff Present:

1. Ms Neha Agrawal
2. Mr Vishnu D. Sharma
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Students record available & checked properly	Letter send to faculty to keep 5 Record of last year	5 best record of last year are available.
2	Kits are required as per RTU syllabus	Letter send to lab in charge and lab technician to prepare Note sheet according to the RTU syllabus and generate requirement	Requirement generated
3	Lab manuals are not updated	Letter send to lab in charge regarding update of the lab manual and also increase the availability of lab manual copy for students	Lab manual update

Signature of Lab Technician

Signature of Lab In charge

Signature of the Lab Audit Experts

(1)

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(3)

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**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Control System Lab

Lab In charge: Mr Ashok Singh

Lab Technician: Mr Dileep S. Jadoun

Audit Date: 20/09/2019


Session: 2019-2020

Members of Staff Present:


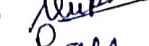

1. Mr Vishnu D Sharma
2. Ms Nupur Yadav
3. Mr Rakesh Kardam


Sr. No.	Comments	Action Taken	Remark
1	Display Board maintained but safety instructions for the lab is missing	Letter send to lab technician to maintain the notice board display	Display board updated
2	Licence version of MATLAB software is not available.	Letter send to lab in charge and lab technician to generate requirement	Requirement generated
3	Students record available & checked properly	Letter send to faculty to keep 5 Record of last year	5 best record of last year are available.


Signature of Lab Technician


Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) 
- (2) 
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Signature of the HOD

Head of the Department
Electrical Engineering

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**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: System Programming lab

Lab In charge: Dr Prerak Bhardwaj

Lab Technician: Mr Harsh Rawat

Audit Date: 13/09/19

Session: 2019-2020

Members of Staff Present:


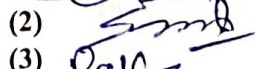
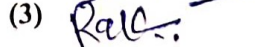
1. Mr Vishal Sharma
2. Mr Shailendra Shrivastava
3. Mr Rakesh Kardam

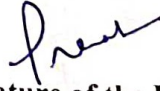
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1	Display Board maintained but safety instructions for the lab is missing	Letter send to lab technician to maintain the notice board display	Display board updated
2	Students record available & checked properly	Letter send to faculty to keep 5 Record of last year	5 best record of last year are available.
3	Licence version of MATLAB software is not available.	Letter send to lab in charge and lab technician to generate requirement	Requirement generated
4	Manual of one experiment is missing	Letter send to lab in charge to properly maintain the lab manuals	Manuals are maintained properly


Signature of Lab Technician


Signature of Lab In charge

Signature of the Lab Audit Experts

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- (2) 
- (3) 


Signature of the HOD

Head of the Department
Electrical Engineering
JECRC

JAIPUR ENGINEERING COLLEGE AND RESEARCH CENTRE
JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

Department of Electrical Engineering
Lab Audit for year (2019 - 20)

Name of the Department: Electrical Engineering
Name of Laboratory: Power System II
Lab In charge: Mr Rahul Kumar Malee
Lab Technician: Mr Harsh Rawat
Audit Date: 17/09/19

Session: 2019-2020

Members of Staff Present

1. Mr Vishnu D. Sharma
2. Ms Neha Agarwal
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Students record available & checked properly	Letter send to faculty to keep 5 Record of last year	5 best record of last year are available.
2	Licence version of MATLAB software is not available.	Letter send to lab in charge and lab technician to generate requirement	Requirement generated
3	Manuals are not maintained properly.	Letter send to lab in charge to properly maintain the lab manuals	Manuals are maintained properly
4	Display Board maintained but roaster for the lab is missing	Letter send to lab technician to maintain the notice board display	Display board updated

Harsh
Signature of Lab Technician

Rahul
Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) *Harsh*
- (2) *Neha*
- (3) *Rahul*

Pranav
Signature of the HOD

Head of the Department
Electrical Engineering
JECRC

JAIPUR ENGINEERING COLLEGE AND RESEARCH CENTRE

JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Electric Drives lab

Lab In charge: Ms Nupur Yadav

Lab Technician: Mr Rajkumar

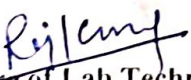
Audit Date: 19/09/19

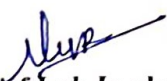
Session: 2019-2020

Members of Staff Present

1. Mr Gopal Tiwari
2. Ms Neha Agarwal
3. Mr Rakesh Kardam


Sr. No.	Comments	Action Taken	Remark
1	RTU syllabus and Scheme for lab are missing on the display board of the lab.	Letter send to lab technician to maintain the notice board and display RTU scheme	Display board updated
2	Lab manuals are not updated	Letter send to lab in charge regarding update of the lab manual and also increase the availability of lab manual copy for students	Lab manual update
3	Kits are required as per RTU syllabus	Letter send to lab in charge and lab technician for prepare Note sheet according to lab kit requirement as per RTU syllabus and generate requirement	Requirement generated
4	Consumable item of lab like patch cords and wires are found missing or loosely connected with the machines.	Letter send to lab in charge and lab technician for proper maintenance of the lab and preparing note sheet according to Lab requirement	Requirement has been full fill
5	Bill available	No action require	Advice to maintain the bill file
6	Stock register verified and all item are matched with stock register entry	Letter for physical verification given to lab in charge	Physical verification Has been done


Signature of Lab Technician


Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) 
- (2) 
- (3) 


Signature of the HOD

**Head of the Department
Electrical Engineering**

JAIPUR ENGINEERING COLLEGE AND RESEARCH CENTRE

JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

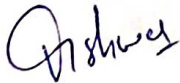
Name of the Department: Electrical Engineering
Name of Laboratory: Power system protection lab
Lab In charge: Mr Shailendra Shrivastava
Lab Technician: Mr Vishwas Verma
Audit Date: 24/09/19

Session: 2019-2020

Members of Staff Present

1. Ms Sonali Chadha
2. Ms Vishnu D.Sharma
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Display Board maintained but roaster for the lab is missing	Letter send to lab technician to maintain the notice board display	Display board updated
2	Kits are required as per RTU syllabus	Letter send to lab in charge and lab technician to prepare note sheet according to lab kit requirement as per RTU syllabus and generate requirement	Requirement generated
3	Manuals are not maintained properly	Letter send to lab in charge to properly maintain the lab manuals	Manuals are maintained properly



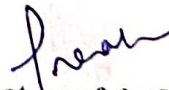
Signature of Lab Technician



Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) 
- (2) 
- (3) 



Signature of the HOD

**Head of the Department
Electrical Engineering
JECRC**

JAIPUR ENGINEERING COLLEGE AND RESEARCH CENTRE

JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**


Name of the Department: Electrical Engineering
Name of Laboratory: Modelling and simulation lab
Lab In charge: Ms Ritu Soni
Lab Technician: Mr Harsh Rawat
Audit Date: 25/09/19

Session: 2019-2020

Members of Staff Present

1. Mr Sunil Ku. Sharma
2. Mr L. Senthil
3. Mr Rakesh Kardam

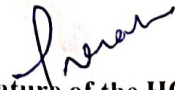
Sr. No.	Comments	Action Taken	Remark
1	Display Board maintained but RTU scheme and syllabus for the lab is missing	Letter send to lab technician to maintain the notice board display	Display board updated
2	Licence version of MATLAB software is not available.	Letter send to lab in charge and lab technician to generate requirement	Requirement generated
3	Students record available & checked properly	Letter send to faculty to keep 5 Record of last year	5 best record of last year are available.


Signature of Lab Technician


Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) 
- (2) 
- (3) 


Signature of the HOD

**Head of the Department
Electrical Engineering**



JAIPUR ENGINEERING COLLEGE
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JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Power system planning lab

Lab In charge: Mr. Atul Kulshrestha

Lab Technician: Mr. Sombir Sharma

Audit Date: 07/09/19

Session: 2019-2020

Members of Staff Present

1. Mr. Vishal Sharma
2. Mr. Ashok S. Chundawat
3. Mr. Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Manuals are not maintained properly	Letter send to lab in charge to properly maintain the lab manuals	Manuals are maintained properly
2	Display Board maintained but lab roaster is missing	Letter send to lab technician to maintain the notice board display	Display board updated
3	Students record available & checked properly	Letter send to faculty to keep 5 record of last year	5 best record of last year are available.

Signature of Lab Technician

Signature of Lab In charge

Signature of the Lab Audit Experts

(1)

(2)

(3)

Signature of the HOD
Head of the Department
Electrical Engineering
JECRC Jaipur



JAIPUR ENGINEERING COLLEGE
AND RESEARCH CENTRE

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JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: Power system modelling and simulation lab

Lab In charge: Mr. L. Senthil

Lab Technician: Mr. Dileep singh

Audit Date: 05/09/2019

Session: 2019-2020

Members of Staff Present

1. Mr. Vishal Sharma
2. Mr. Rahul Kumar Malee
3. Mr. Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Students record available & checked properly	Letter send to faculty to keep 5 Record of last year	5 best record of last year are available.
2	Licence version of MATLAB software is not available.	Letter send to lab in charge and lab technician to generate requirement	Requirement generated
3	Display Board maintained but RTU scheme, syllabus and roster for the lab is missing	Letter send to lab technician to maintain the notice board display	Display board updated

Sing
Signature of Lab Technician

[Signature]
Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) *[Signature]*
- (2) *[Signature]*
- (3) *[Signature]*

[Signature]
Signature of the HOD

Head of the Department
Electrical Engineering
JECRC



JAIPUR ENGINEERING COLLEGE
AND RESEARCH CENTRE

JAIPUR ENGINEERING COLLEGE AND RESEARCH CENTRE

JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering

Name of Laboratory: CBPS Lab

Lab In charge: Mr. L. Senthil

Lab Technician: Mr. Dileep singh

Audit Date: 06/09/19

Session: 2019-2020

Members of Staff Present




1. Ms. Sonali Chadha
2. Ms. Ritu Soni
3. Mr. Rakesh Kardam


Sr. No.	Comments	Action Taken	Remark
1	Students record available & checked properly	Letter send to faculty to keep 5 Record of last year	5 best record of last year are available.
2	Licence version of MATLAB software is not available.	Letter send to lab in charge and lab technician to generate requirement	Requirement generated
3	Manuals are not maintained properly	Letter send to lab in charge to properly maintain the lab manuals	Manuals are maintained properly

Signature of Lab Technician 

Signature of Lab In charge 

Signature of the Lab Audit Experts

(1) 
(2) 
(3) 

Signature of the HOD 

Head of the Department
Electrical Engineering
JECRC Jaipur



JAIPUR ENGINEERING COLLEGE AND RESEARCH CENTRE

JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

Name of the Department: Electrical Engineering
Name of Laboratory: Electric drive and their control
Lab In charge: Mr. Rahul Ku. Malee
Lab Technician: Mr. Rajkumar
Audit Date: 13/09/2019

Session: 2019-2020

Members of Staff Present

1. Ms. Neha Agarwal
2. Ms. Nupur Yadav
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Stock register verified and all item are matched with stock register entry	Letter for physical verification given to lab in charge	Physical verification has been done
2	RTU syllabus and Scheme for lab are missing on the display board of the lab.	Letter send to lab technician to maintain the notice board and display RTU scheme	Display board updated
3	Lab manuals are not updated	Letter send to lab in charge regarding update of the lab manual and also increase the availability of lab manual copy for students	Lab manual update
4	Kits are not available as per RTU syllabus	Letter send to lab in charge and lab technician for prepare Note sheet according to lab kit requirement as per RTU syllabus and generate requirement	Requirement generated
5	Consumable item of lab like patch cords and wires are found missing or loosely connected with the machines.	Letter send to lab in charge and lab technician for proper maintenance of the lab and preparing note sheet according to Lab requirement	Requirement has been full fill
6	Bill available	No action require	Advice to maintain the bill file

Signature of Lab Technician
Rajkumar

Signature of Lab In charge
Rahul

Signature of the Lab Audit Experts

- (1) *Neha*
- (2) *Nupur*
- (3) *Rakesh*

Signature of the HOD
Prasen
Head of the Department
Electrical Engineering
JECRC Jaipur



JAIPUR ENGINEERING COLLEGE
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JECRC Campus, Shri Ram Ki Nangal, Via-Vatika, Jaipur

**Department of Electrical Engineering
Lab Audit for year (2019 - 20)**

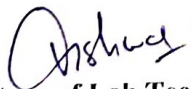
Name of the Department: Electrical Engineering
Name of Laboratory: High voltage engineering lab
Lab In charge: Mr Shailendra Shrivastava
Lab Technician: Mr Viswas Verma
Audit Date: 13/09/2019


Session: 2019-2020

Members of Staff Present


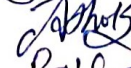
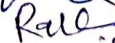
1. Mr Vishnu Dutt Sharma
2. Mr Ashok S. Chundawat
3. Mr Rakesh Kardam

Sr. No.	Comments	Action Taken	Remark
1	Lab manuals are not updated	Letter send to lab in charge regarding update of the lab manual and also increase the availability of lab manual copy for students	Lab manual update
2	kits are required as per RTU syllabus	Letter send to lab in charge and lab technician for prepare Note sheet according to lab kit requirement as per RTU syllabus and generate requirement	Requirement generated
3	Lab roaster is missing on the display board of the lab.	Letter send to lab technician to maintain the notice board.	Display board updated


Signature of Lab Technician


Signature of Lab In charge

Signature of the Lab Audit Experts

- (1) 
- (2) 
- (3) 


Signature of the HOD
Head of the Department
Electrical Engineering
JECRC Jaipur